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| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
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| 10/677,392 | 10/01/2003 | Martin H. Graham | 3921P007 | 9543 |
| 8791 | 7590 09/10/2004 | | EXAMINER | |
| BLAKELY SOKOLOFF TAYLOR & ZAFMAN | | | KITOV, ZEEV | |
| 12400 WILS | SHIRE BOULEVARD FLOOR | | ART UNIT | PAPER NUMBER |
| | LES, CA 90025-1030 | | 2836 | |
| | • | | DATE MAILED: 09/10/200- | 4 |

Please find below and/or attached an Office communication concerning this application or proceeding.

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|--|---|---|-------------------------|
| | Application No. | Applicant(s) | |
| | 10/677,392 | GRAHAM, MARTIN | H. |
| Office Action Summary | Examiner | Art Unit | |
| | Zeev Kitov | 2836 | |
| The MAILING DATE of this communicate Period for Reply | tion appears on the cover sheet wi | th the correspondence addr | ress |
| A SHORTENED STATUTORY PERIOD FOR THE MAILING DATE OF THIS COMMUNICA - Extensions of time may be available under the provisions of 3 after SIX (6) MONTHS from the mailing date of this communic - If the period for reply specified above is less than thirty (30) da - If NO period for reply is specified above, the maximum statuto - Failure to reply within the set or extended period for reply will, Any reply received by the Office later than three months after the earned patent term adjustment. See 37 CFR 1.704(b) | TION. 7 CFR 1.136(a). In no event, however, may a relation. ays, a reply within the statutory minimum of thirtry period will apply and will expire SIX (6) MON by statute, cause the application to become AB | eply be timely filed y (30) days will be considered timely. THS from the mailing date of this com ANDONED (35 U.S.C. § 133). | munication. |
| Status | | | |
| 1) Responsive to communication(s) filed of 2a) This action is FINAL. 2b) Since this application is in condition for closed in accordance with the practice of the second se | ☐ This action is non-final. allowance except for formal matte | • • | nerits is |
| Disposition of Claims | | | |
| 4) ☐ Claim(s) 1 - 12 is/are pending in the apple 4a) Of the above claim(s) is/are versions 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 1 - 12 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restrictions | vithdrawn from consideration. | | |
| Application Papers | | | |
| 9) ☐ The specification is objected to by the E. 10) ☑ The drawing(s) filed on 01 October 2003 Applicant may not request that any objection Replacement drawing sheet(s) including the 11) ☐ The oath or declaration is objected to by | 3 is/are: a) \square accepted or b) \square of to the drawing(s) be held in abeyant correction is required if the drawing(| ce. See 37 CFR 1.85(a). s) is objected to. See 37 CFR | ł 1.121(d). |
| Priority under 35 U.S.C. § 119 | | | |
| 12) Acknowledgment is made of a claim for a) All b) Some * c) None of: 1. Certified copies of the priority doc 2. Certified copies of the priority doc 3. Copies of the certified copies of the application from the International * See the attached detailed Office action for | cuments have been received. cuments have been received in Aphe priority documents have been Bureau (PCT Rule 17.2(a)). | oplication No received in this National St | tage |
| Attachment(s) | _ | | |
| Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-3) Information Disclosure Statement(s) (PTO-1449 or PTO Paper No(s)/Mail Date | 948) Paper No(s | ummary (PTO-413))/Mail Date formal Patent Application (PTO-1 | 52) |

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DETAILED ACTION

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1, 4 - 8 and 10 are rejected under 35 U.S.C. 103(a) as being unpatentable over Graham (US 6,424,125) in view of Shilling et al. (US 3,763,395). Regarding Claims 1 and 7, Graham discloses most of the structural limitations of the claim including a capacitor (element 10 in Fig. 1) attenuating signals having a frequency higher than the fundamental frequency; a control device (element 14 in Fig. 1) having a gate and two terminals, the two terminals being coupled to the capacitor; and a resistor (elements 11 in Fig. 1) coupled between the gate and one of the terminals of the control device causing the control device to conduct in the presence of unusually high voltage. However, it does not disclose a variable resistor. Shilling et al. discloses the variable resistor (elements 576, 577 in Fig. 5) coupled between the gate and one of the terminals of the control device causing the control device to conduct in the presence of unusually high voltage. Both references have the same problem solving area, namely overvoltage protection. Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to have modified the Graham solution by

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adding the variable resistors according to Shilling et al., because as well known in the art, such resistors are used to adjust the firing voltage threshold of the TRIAC.

Regarding Claims 4 and 8, Graham discloses the control device as TRIAC (element 14 in Fig. 1).

Regarding Claim 5, Shilling et al. disclose the two terminals of the control device as an anode terminal and cathode terminal (see Fig. 5), and the variable resistor (elements 576, 577) being coupled between the control electrode (node G in Fig. 5) and the anode (node T2 in Fig. 5). A motivation for modification of the primary reference is the same as above.

Regarding Claims 6 and 10, Graham discloses a resistor (element (element 16 in Fig. 1) coupled in series with the control device (element 14 in Fig. 1). Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to have further modified the Shilling solution by adding the resistor in series with the control device according to Graham, because as Graham states (col. 2, lines 53 – 56), it is necessary for dissipation the capacitor charge when it is discharging.

Claims 2, 9 and 12 are rejected under 35 U.S.C. 103(a) as being unpatentable over Graham in view of Shilling et al. and Mura (US 4,216,756). As was stated above, Graham and Shilling et al. disclose all the elements of Claims 1, 7 and 8. However, regarding Claims 2, 9 and 12, they do not disclose the varistor. Mura discloses the varistor (element 50b in Fig. 2) coupled between the anode and the gate terminals of the TRIAC (element 42b in Fig. 2, col. 6, lines 10 – 22). Both references have the same

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problem solving area, namely protecting electronic equipment against over-voltages. Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to have further modified the Shilling et al. solution by adding the varistor according to Mura, because as Mura states (col. 3, lines 35 – 39), the varistor helps to protect the circuit against excessive power dissipation. And as well known in the art, the varistor is widely used to protect circuits against over-voltages, while protection of the TRIAC gate is especially important, due to its high vulnerability to the excessive voltages.

Claims 3 and 11 are rejected under 35 U.S.C. 103(a) as being unpatentable over Graham in view of in view of Shilling et al. and Court Decision *In re Boesch*, 617 F.2d 272, 205 USPQ 215 (CCPA 1980). As was stated above, Graham and Shilling et al. disclose all the elements of Claims 1 and 7. However, regarding Claims 3 and 11, they do not disclose the varistor with cross-bar characteristics. As to particular characteristics of the Claims 3 and 10 varistor ("cross-bar varistor"), the Specification does not disclose them only saying that: "once triggered the varistor places essentially, a short across the power source" (page 2, paragraph 007). As well known in the art, to fire the TRIAC, the current is to be delivered to the TRIAC's gate, i.e. relatively low resistance is to be placed between the supply voltage source and the TRIAC gate. Therefore, a value of the resistance of the active varistor can affect the result (firing of TRIAC) and represents a result effective variable. Court Decision addresses finding a value of the result effective variable by stating that discovering an optimum value of a result effective

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variable involves only routine skill in the art. Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to have modified the Shilling et al. solution by selecting the varistor with certain active characteristics ("crossbar varistor"), because as it has been held that discovering an optimum value of a result effective variable involves only routine skill in the art.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Zeev Kitov whose current telephone number is (571) 272 - 2052. The examiner can normally be reached on 8:00 – 4:30. If attempts to reach examiner by telephone are unsuccessful, the examiner's supervisor, Brian Sircus can be reached on (571) 272 – 2800, Ext. 36. The fax phone number for organization where this application or proceedings is assigned is (703) 872-9306 for all communications.

Z.K. 08/27/2004

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